

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	91	fet with bjt with gate with base	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/10 13:44
L6	32	fet with bjt with gate with base with emitter with source with collector with drain	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/10 13:45
S1	27	frequency adj doubler same phase adj revers\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/08 12:04
S2	0	frequency adj doubler same phase adj revers\$3 and differential adj input and differential adj output	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/18 14:22
S3	20	frequency adj doubler same phase adj revers\$3 and differential	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/18 14:24
S4	0	differential adj frequency adj doubler	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/18 14:22
S5	76	differential with frequency adj doubler	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/18 14:24
S11	7093	phase adj revers\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/08 12:04
S16	708	inverting adj transistor	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/08 12:24